

Digital Full Adders & Subtractors Using NAND Gates



Product Categories: [Digital Electronics Lab](#), [Engineering Equipment](#)

Product Page:

<https://www.labappara.com/product/digital-full-adders-subtractors-using-nand-gates/>

Product Description

Digital Full Adders & Subtractors Using NAND Gates

Features :

Instrument comprises of DC Regulated Power Supply 5VDC/150mA for logic inputs, 3 SPDT switches provided for selecting logic '1' & logic '0', 3 NOT gates for providing compliments of the logic inputs, 2 Red LED output indicator, circuit diagram printed for 5 three input 'NAND' gates, 3 two input 'NAND' gates & 1 four inputs 'NAND' gates & their respective IC's placed inside the cabinet & connections brought out at sockets.