

Flip Flops Using NAND Gates & TTL IC's



Product Categories: [Digital Electronics Lab](#), [Engineering Equipment](#)

Product Page:

<https://www.labappara.com/product/flip-flops-using-nand-gates-ttl-ics/>

Product Description

Flip Flops Using NAND Gates & TTL IC's

Computer Logic Training Board on Flip-Flops has been specifically designed to give students an idea about Flip- Flops and to study different types of Flip-Flops. The output of the Flip-Flops can be observed with the help of logic level indicators (LEDs), which are provided on the panel. The board is absolutely self contained and requires no other apparatus. Practical experience on this board carries great educative value for Science and Engineering Students.

Features

The board consists of the following built-in parts :

1. 5V D.C. at 100mA, IC regulated power supply.
2. Four Input Switches for data input.
3. Eight, 2-input NAND gates.
4. Four, 3-input NAND gates.

5. One inverter (NOT gate).
7. One 7476 IC Provided on board for JK and T type Flip Flop.
8. One 7474 IC Provided on the board for D Type Flip Flop.
9. Two LEDs with driver circuit to observe the output of flip-flops.
10. A Pulser to provide the pulses manually for triggering.
11. Adequate no. of other Electronic Components.
12. Mains ON/OFF switch, Fuse and Jewel light.

The unit is operative on 230V $\pm 10\%$ at 50Hz A.C. Mains.

Adequate nos. of patch cords stackable from rear both ends 4mm spring loaded plug, length $\frac{1}{2}$ metre

Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.

Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Weight : 2 Kg. (Approx.).

Dimension : W 300 x H 110 x D 200